

IN THE CLAIMS

Please amend the claims as follows:

Claims 1-8 (Canceled).

Claim 9 (Currently Amended): An amplifier comprising:

an amplification device, an output terminal of the amplification device being an output terminal of the amplifier, a load to the amplifier being directly coupled to the amplification device;

an LC parallel resonant circuit connected in parallel to the amplification device; and

an LCR series resonant circuit connected in parallel to the amplification device and the LC parallel resonant circuit.

Claim 10 (Previously Presented): An amplifier according to claim 9, wherein a common-gate circuit and a cascade circuit are combined.

Claim 11 (Previously Presented): An amplifier according to claim 9, wherein a common-source circuit, a cascade circuit, and a voltage feedback circuit are combined.

Claim 12 (Currently Amended): A wireless communication apparatus comprising:

an antenna;

a band-pass filter;

a low noise amplifier configured to amplify a voltage of a received signal and

including

an amplification device, an output terminal of the amplification device being

an output terminal of the low noise amplifier,

an LC parallel resonant circuit connected in parallel to the amplification device, and

an LCR series resonant circuit connected in parallel to the amplification device and the LC parallel resonant circuit;

a down-converter connected to the output terminal of the low noise amplifier and configured to down-convert the voltage-amplified received signal by frequency conversion, the down-converter being directly coupled to the amplification device;

an automatic gain controller;

an analog-digital converter; and

a signal processing circuit configured to perform digital signal processing of received data.

Claim 13 (Currently Amended): A wireless communication apparatus comprising:

an antenna;

a band-pass filter;

a low noise amplifier configured to amplify a voltage of a received signal and

including

an amplification device, and output of the amplification device being an output of the low noise amplifier,

an LC parallel resonant circuit connected in parallel to the amplification device, and

an LCR series resonant circuit connected in parallel to the amplification device and the LC parallel resonant circuit;

a down-converter connected to the output of the low noise amplifier and configured to down-convert the voltage-amplified received signal by frequency conversion;

an automatic gain controller;

an analog-digital converter;

a digital-analog converter configured to convert transmit data to an analog signal, the down-converter being directly coupled to the amplification device;

an up-converter configured to up-convert the analog transmit signal by frequency conversion;

a power amplifier configured to amplify a power of the up-converted transmit signal;

and

a signal processing circuit configured to perform digital signal processing of transmit/receive data.

Claim 14 (Currently Amended): An amplifier comprising:

an amplification device, an output terminal of the amplification device being an output terminal of the amplifier, a load to the amplifier being directly coupled to the amplification device; and

[[a]] an analog band-pass filter connected in parallel to the output terminal of the amplification device, the analog band-pass filter having a plurality of poles provided on a left side of an s-plane and a plurality of zeros arranged between the poles, at least two zeros being arranged at locations other than an origin of the s-plane.

Claim 15 (Previously Presented): An amplifier according to claim 14, wherein the band-pass filter does not have a capacitor provided in series with an output terminal of the amplifier.

Claim 16 (Previously Presented): An amplifier according to claim 14, wherein an inductance and a capacitor are not provided in series between an output terminal of the amplification device and an output terminal of the amplifier.

Claim 17 (Previously Presented): An amplifier according to claim 14, wherein a common-gate circuit and a cascade circuit are combined.

Claim 18 (Previously Presented): An amplifier according to claim 14, wherein a common-source circuit, a cascade circuit, and a voltage feedback circuit are combined.

Claim 19 (Currently Amended): A wireless communication apparatus comprising:
an antenna;
a band-pass filter;
a low noise amplifier configured to amplify a voltage of a received signal and
including

an amplification device, an output terminal of the amplification device being
an output terminal of the low noise amplifier, and

[[a]] an analog band-pass filter connected in parallel to the output terminal of
the amplification device, the analog band-pass filter having a plurality of poles
provided on a left side of an s-plane and a plurality of zeros arranged between the
poles, at least two zeros being arranged at locations other than an origin of the s-
plane;

a down-converter configured to down-convert the voltage-amplified received signal
by frequency conversion, the down-converter being directly coupled to the amplification
device;

an automatic gain controller;
an analog-digital converter; and
a signal processing circuit configured to perform digital signal processing of received data.

Claim 20 (Currently Amended): A wireless communication apparatus comprising:
an antenna;
a band-pass filter;
a low noise amplifier configured to amplify a voltage of a received signal and including

an amplification device, an output terminal of the amplification device being an output terminal of the low noise amplified and

[[a]] an analog band-pass filter connected in parallel to the output terminal of the amplification device, the analog band-pass filter having a plurality of poles provided on a left side of an s-plane and a plurality of zeros arranged between the poles, at least two zeros being arranged at locations other than an origin of the s-plane;

a down-converter configured to down-convert the voltage-amplified received signal by frequency conversion, the down-converter being directly coupled to the amplification device;

an automatic gain controller;
an analog-digital converter;
a digital-analog converter configured to convert transmit data to an analog signal;

an up-converter configured to up-convert the analog transmit signal by frequency conversion;

a power amplifier configured to amplify a power of the up-converted transmit signal;
and

a signal processing circuit configured to perform digital signal processing of transmit/receive data.